

change instruction by executing a predetermined change instruction (S1330 and S1530 in FIG. 12). Accordingly, the change instruction for changing the baud rate (communication speed) is disposed in the rewrite-control program sent from the memory-rewriting device 4 prior to the instruction set for performing data-rewrite processing (FIG. 13) to rewrite the data within the flash ROM 20a with new data sent from the memory-rewriting device 4, as shown in S1530 of FIG. 12. That is, the change instruction for changing the baud rate (labeled “COMMUNICATION SPEED CHANGE SECTION” in exemplary FIG. 2) is disposed in the rewrite control program prior to the instruction set for performing data-rewrite processing (labeled “REWRITE INSTRUCTION SECTION” in exemplary FIG. 2) shown in FIG. 13.--

REMARKS

Reconsideration and allowance of this application are respectfully requested. Currently, claims 22-24 and 32-33 are pending in this application.

The Examiner is thanked for her indication that claims 22-24 and 32-33 are allowable.

Attached hereto is a marked-up version of the changes made to the specification by the current Amendment. The attached is captioned “**Version With Markings to Show Changes Made.**”

Objections to the Drawings and Specification:

The application was objected to because of various formal matters.

In response to section 3(b) of the Office Action, Applicant has amended the specification to update the statuses of the parent applications in the first page of the specification.

In section 3(a) of the Office Action, the drawings were objected to because they did not allegedly show the claimed features “rewrite control program including a communication speed change section, including...rewrite instruction section,” as required by claim 22 and “communication speed change instruction...preceding the rewrite instruction section in the rewrite control program,” as recited in claim 24. In section 3(c) of the Office Action, the specification was objected to as allegedly failing to provide a proper antecedent basis for these claimed features.

Applicant has editorially amended the drawings and specification to explicitly show these claimed features in the drawings and to provide a proper antecedent basis for these features in the specification. For example, in the attached Drawing Change Authorization Request, revisions have been proposed to Fig. 2 in order to show a “communication speed change section” and a “rewrite instruction section”. The specification has been editorially revised to ensure that these terms have a proper antecedent basis. Applicant submits that no new matter has been added to the specification or drawings.

With respect to the feature “communication speed change instruction...**preceding** the rewrite instruction section in the rewrite control program (emphasis added),” as required by claim 24, this feature is shown in Figs. 12-13 taken together. Specifically, as described in pages 52-53 of the

specification, the communication speed change section is executed in steps S1330 and S1530 in Fig. 12 which precede the rewrite instruction set illustrated in Fig. 13. (See, e.g., Figs. 12-13 and page 52, lines 10-20 in particular).

Applicant requests that the proposed drawing changes provided in the Drawing Change Authorization Request be approved for entry into this application. Applicant further requests that the objection to the drawings and specification be withdrawn.

Request for Refund:

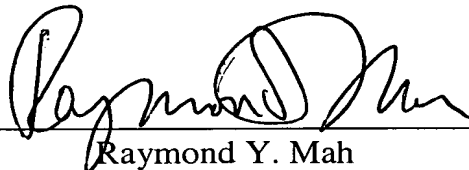
Applicant filed a Request for Refund on November 25, 2002. Applicant respectfully requests action for this Request.

Conclusion:

Applicant believes that this entire application is in condition for allowance and respectfully requests a notice to this effect. If the Examiner has any questions or believes that an interview would further prosecution of this application, the Examiner is invited to telephone the undersigned.

Respectfully submitted,

NIXON & VANDERHYE P.C.

By: 
Raymond Y. Mah
Reg. No. 41,426

RYM/sl
1100 North Glebe Road, 8th Floor
Arlington, VA 22201-4714
Telephone: (703)816-4044
Facsimile: (703)816-4100

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION:

Paragraph beginning at page 1, line 5 has been amended as follows:

This application is a division of Application No. 09/625,403, filed July 25, 2000 and now U.S. Patent No. 6,477,626, which is a divisional of Application no. 08/934,220 filed September 19, 1997, still pending, the entire content of which is hereby incorporated by reference in this application.

Paragraph beginning at page 10, line 14 has been amended as follows:

As shown in FIG. 2, a starting address of the copy destination of the write-control program in the microprocessor 8 on the ECU 2 side (that is, an address where storage of the write-control program in the RAM 22 on the ECU 2 side is to start), and similarly, an end address of the copy destination of the write-control program, a starting address of the write destination of the write data in the microprocessor 8 on the ECU 2 side (that is, an address where writing of the write data to the flash ROM 20a on the ECU 2 side is to start), and similarly, an end address of the write destination of the write data.

Additionally, [although not] as illustrated in FIG. 2, information and the like relating to the communication protocol (communication speed, communication format, and so on) of data communication between the memory-writing device 4 and the ECU 2 also is stored in the parameter storage region M2 as other control parameters.

Paragraph beginning at page 52, line 10 has been amended as follows:

Meanwhile, moreover, the ECU 2 according to this embodiment is structured so that the baud rate of data communication performed with the memory-rewriting device 4 is a value which is variable in accordance with a change instruction by executing a predetermined change instruction (S1330 and S1530 in FIG. 12). Accordingly, the change instruction for changing the baud rate (communication speed) is disposed in the rewrite-control program sent from the memory-rewriting device 4 prior to the instruction set for performing data-rewrite processing (FIG. 13) to rewrite the data within the flash ROM 20a with new data sent from the memory-rewriting device 4, as shown in S1530 of FIG. 12. That is, the change instruction for changing the baud rate (labeled “COMMUNICATION SPEED CHANGE SECTION” in exemplary FIG. 2) is disposed in the rewrite control program prior to the instruction set for performing data-rewrite processing (labeled “REWRITE INSTRUCTION SECTION” in exemplary FIG. 2) shown in FIG. 13.